



#10/Amat
PATENT 2/25/03
81784.0224 K. P. Hall

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Ryoichi YOKOYAMA

Serial No. 09/747,194

Filed: December 22, 2000

For: POWER CONSUMPTION OF DISPLAY
APPARATUS DURING STILL IMAGE
DISPLAY MODE

Art Unit: 2673

Examiner: Leonid Shapiro

AMENDMENT

Commissioner for Patents
Washington, D.C. 20231

I hereby certify that this correspondence
is being deposited with the United States
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Commissioner for Patents
Washington D.C. 20231, on

February 10, 2003

Date of Deposit

John P. Schenklacher, Reg. No. 23,009

Name

Signature

Date

Dear Sir:

In response to the Office Action dated September 9, 2002, the period for
response to which is being extended for 2 months to February 9, 2003 by the
accompanying Petition, please amend the above-identified application as follows:

IN THE SPECIFICATION:

Please rewrite the Abstract Of The Disclosure as follows:

RECEIVED

FEB 24 2003

Technology Center 2600

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02 FC:1202
03 FC:1201

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ABSTRACT OF THE DISCLOSURE

Al Cont
A plurality of gate lines connected to a gate driver for supplying gate signals
and a plurality of drain lines connected to a drain driver for supplying drain signals
are provided on a substrate. Pixels are formed in the regions surrounded by these
lines. Each of the pixels includes a TFT, a storing circuit connected to the source of
the TFT for storing a digital signal, and a signal selector for selecting a signal A or
signal B in response to the signal stored in the storing circuit and supplying the
selected signal to a display electrode. Once a digital signal corresponding to a
display image is written to the storing circuit of each pixel, an image can be